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	Application No.	Applicant(s)
Notice of Allowability	10/699,838	MAMITSU ET AL.
	Examiner	Art Unit
	Roy K Potter	2822
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to		
2. ☑ The allowed claim(s) is/are <u>66 and 67</u> .		
3. 🔀 The drawings filed on <u>04 November 2003</u> are accepted by the Examiner.		
 4. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a)		
Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/06 Paper No./Mail Date 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. ☐ Interview Summary (Paper No./Mail Date 8), 7. ☐ Examiner's Amendm	ė

U.S. Patent and Trademark Office PTOL-37 (Rev. 1-04)

REASONS FOR ALLOWANCE

The following is an examiner's statement of reasons for allowance:

The prior art does not teach of suggest the recited semiconductor device comprising first and second radiation members thermally and electrically connected to a semiconductor chip and resist assist bonding resin disposed in spaces between bump shaped bonding members on a surface of one of the first and second radiation members.

Lee, U.S. Patent No. 4620215, discloses an integrated circuit package with two heat sinks and a chip located there between.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Roy K Potter whose telephone number is 571 272 1842. The examiner can normally be reached on M-F.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Roy K Potter Primary Examiner

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